CLAIMS:

1. A scalable system chip (1) for coupling at least two data bus systems, with at least one transceiver (2; 3), integrated on the system chip, which is provided to create a coupling with a data bus of a first type, and with at least one controller (4), integrated on the system chip, to control at least one external transceiver (8; 9; 10), which is provided for coupling with a data bus of a second type.

2. A scalable system chip (1) as claimed in claim 1, characterized in that the system chip (1) is equipped with a terminal connection for an external microcontroller (11), which is controlled by the system chip (1) in respect of power supply, reset and interrupt, and which processes at least parts of the send and/or receive protocols of the internal and external transceivers (8; 9; 10).

3. A scalable system chip (1) as claimed in claim 2, characterized in that, in the event of a failure of the external microcontroller (11), the system chip (1) assumes basic control tasks for the internal transceiver (2; 3) and for the control of the external transceiver (8; 9; 10).

4. A scalable system chip (1) as claimed in claim 1, characterized in that send and/or receive signals exchanged between the external microcontroller (11) and the internal (2; 3) and external (8; 9; 10) transceivers are routed via the system chip (1), in which a level adaptation of these signals takes place if applicable.

5. A use of a system chip (1) as claimed in any one of claims 1 to 4 in a vehicle, for coupling multiple data bus systems of different types provided in the vehicle.